

PATENT



DOCKET NO.: Intel 2207/10083  
ASSIGNEE: Intel Corporation

2133  
41

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant No. : 09/751,750 Confirmation No. 5033

Applicant : Talal K. JABER

Filed : December 29, 2000

For : MICROPROCESSOR ON-CHIP TESTING  
ARCHITECTURE AND IMPLEMENTATION Technology Center 2100

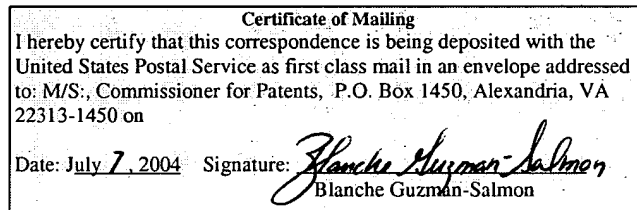
Group Art Unit : 2133

Examiner : Guy J. LAMARRE

**RECEIVED**

JUL 19 2004

M/S:  
COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450



**AMENDMENT**

Sir:

In response to the Office Action issued January 12, 2004, please amend the above-identified application as follows:

**Amendments to the Claims** begin on page 2 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.